

an upper electrode formed on the capacitive insulating film;
a contact layer formed on the upper electrode;
an insulating film formed to cover the lower electrode, the capacitive insulating film, the upper electrode and the contact layer;
a contact hole passing through the insulating film and the contact layer to reach the upper electrode; and
a¹ cont. a metal interconnect, which is defined on a part of the insulating film, provided at least in the contact hole and connected to the upper electrode,
wherein the ferroelectric film includes $\text{SrBi}_2\text{Ta}_2\text{O}_9$, and
wherein the contact layer is a single layer film or a multilayer structure, the single-layer film being made of a metal oxide or a metal nitride, the multilayer structure being made up of metal oxide and metal nitride films.

Please add the following new claims 6-9:

--6. (New) A semiconductor device comprising:
a lower electrode formed on a substrate;
a capacitive insulating film formed out of a ferroelectric film on the lower electrode;
an upper electrode formed on the capacitive insulating film;
a² a contact layer formed on the upper electrode;
an insulating film formed to cover the lower electrode, the capacitive insulating film, the upper electrode and the contact layer;
a contact hole passing through the insulating film and the contact layer to reach the upper

electrode; and

a metal interconnect, which is defined on a part of the insulating film, provided at least in the contact hole and connected to the upper electrode,

11/2/st wherein the ferroelectric film includes [a crystal composed of Sr, Bi, Ta and O₂] and

wherein the contact layer is a single-layer film or a multilayer structure, the single-layer film being made of a metal oxide or a metal nitride, the multilayer structure being made up of metal oxide and metal nitride films.

7. (New) The device of Claim 6,

wherein the upper electrode contains Pt or Ir, and

2. cont wherein the metal oxide film is made of an oxide of Ti or an oxide of Ta, while the metal nitride film is made of a nitride of Ti or a nitride of Ta.

8. (New) A semiconductor device comprising:

a lower electrode formed on a substrate;

a capacitive insulating film formed out of a ferroelectric film on the lower electrode;

an upper electrode formed on the capacitive insulating film;

a contact layer formed on the upper electrode;

an insulating film formed to cover the lower electrode, the capacitive insulating film, the upper electrode and the contact layer;

a contact hole passing through the insulating film and the contact layer to reach the upper electrode; and

a metal interconnect, which is defined on a part of the insulating film, provided at least in